

WEST Search History

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DATE: Wednesday, August 10, 2005

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<input type="checkbox"/>	L7	L6 and speculative	2
<input type="checkbox"/>	L6	L2 and emulator	12
<input type="checkbox"/>	L5	L4 and (cisc or risc)	2
<input type="checkbox"/>	L4	L3 and (code same speculat\$)	6
<input type="checkbox"/>	L3	L2 and (register same (definit\$4 or hdl))	44
<input type="checkbox"/>	L2	L1 and ((exception or interrupt) same handl\$4)	139
<input type="checkbox"/>	L1	(virtual or abstract) adj register	531

END OF SEARCH HISTORY